**Von Newmann’s Architecture:**

<https://www.computerscience.gcse.guru/theory/von-neumann-architecture>

**Flip-flop** is a 1-bit memory cell which can be used for storing the digital data. To increase the storage capacity in terms of number of bits, we have to use a group of flip-flops. Such a group of flip-flops is known as a Register.

**Registers:**

<https://www.educba.com/types-of-registers/>

**Buses:**

<https://www.classmate4u.com/types-of-computer-buses/>

